

114年度台科大 ADFP教學工作坊

10/30

星期四

國際大樓
IB-302會議室

上午場

Full-custom課程

08:30-08:50	報到
08:50-09:00	主持人開場
09:00-09:40	FinFET Brief Introduction & ADFP Collaterals
09:40-10:20	Planar-FET Layout & Planar FET vs FinFET
10:20-10:40	Coffee Break
10:40-11:20	DRM & ICV-Launch DRC, LVS and Star-RC
11:20-12:00	Real N16 design and Layout considerations & SMART Dummy Generation
12:00-12:20	Q & A

下午場

Cell-based課程

12:50-13:10	報到
13:10-13:20	主持人開場
13:20-13:50	Overview and design compiler setup
13:50-14:20	Setting design constraints, Design optimization, and synthesis report and analysis
14:20-14:50	Design flow overview & Design import & Floorplan
14:50-15:10	Coffee Break
15:10-15:40	Innovus (ADFP) Powerplan → Placement → CTS: Key Differences
15:40-16:10	Innovus-routing & power analysis & data out
16:10-16:40	Layout verification - Calibre
16:40-17:00	Q & A

線上報名

